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1. A method for modeling analog signals comprising the steps of:

(B) modeling said attributed analog signals by adding a signature to each of said one or more attributed signals.

performing digital simulations with said one or more attributed analog signals.

4. The method according to claim 1, wherein each of said one or more signatures comprises an unique digital signature.

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9. A method for testing a model of a device, comprising
the steps of:

(B) verifying connectivity of one or more attributed
5 analog signals within said model by verifying one or more
signatures associated with each of said one or more attributed
analog signals.

if said one or more attributed analog signals are not verified, disabling said device.

12. The method according to claim 11, wherein step (B) further comprises verifying a second one or more models of analog blocks configured to receive another at least one of said one or more attributed analog signals.

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13. A method for verifying a model of a device comprising the steps of:

(A) detecting one or more attributed analog signals; and

(B) performing one or more verifications of one or more
5 predetermined parameters in response to said one or more attributed analog signals.

14. The method according to claim 13, wherein step (A) further comprises:

adding a signature to at least one of said one or more attributed analog signals.

15. The method according to claim 14, wherein said signature comprises an unique digital signature.

16. The method according to claim 13, wherein said one or more predetermined parameters comprise one or more digital parameters.

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predetermined parameter of said one or more predetermined
10 parameters.

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